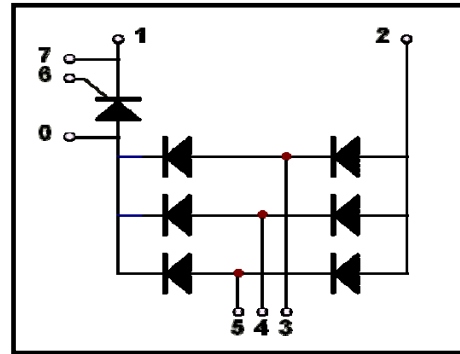


Features

- Isolated Module Package
- Isolation voltage 3000 V
- Three Phase Bridge and a Thyristor

Applications

- Current Stabilized Power Supply
- Switching Power Supply
- Inverter For AC or DC Motor Control



■ Diode

ABSOLUTE MAXIMUM RATINGS

T_C=25°C unless otherwise specified

| Symbol | Parameter | Test Conditions | Max. | Unit |
|--------------------|--------------------------------------|---|-------------|-------------------|
| V _{RRM} | Repetitive Reverse Voltage | | 1600 | V |
| I _{D(AV)} | Average Forward Current | T _C =90°C, module | 75 | A |
| I _{FSM} | Non-Repetitive Surge Forward Current | T _J =45°C, t=10ms, 50Hz, Sine | 1050 | A |
| | | T _J =45°C, t=8.3ms, 60Hz, Sine | 1150 | A |
| I ² t | I ² t (For Fusing) | T _J =45°C, t=10ms, 50Hz, Sine | 5.5 | kA ² s |
| | | T _J =45°C, t=8.3ms, 60Hz, Sine | 5.4 | kA ² s |
| T _J | Junction Temperature | | -40 to +150 | °C |
| T _{STG} | Storage Temperature Range | | -40 to +125 | °C |
| V _{isol} | Insulation Test Voltage | AC, 50Hz, t=1min | 3000 | V |
| Weight | | | 215 | g |

ELECTRICAL AND THERMAL CHARACTERISTICS

T_C=25°C unless otherwise specified

| Symbol | Parameter | Test Conditions | Min. | Typ. | Max. | Unit |
|------------------|-------------------------------------|--|------|------|------|------|
| I _{RM} | Reverse Leakage Current | V _R =1600V | -- | -- | 500 | μA |
| | | V _R =1600V, T _J =125°C | -- | -- | 5 | mA |
| V _F | Forward Voltage | I _F =75A | -- | 1.05 | -- | V |
| | | I _F =75A, T _J =125°C | -- | 1.0 | -- | V |
| R _{θJC} | Thermal Resistance Junction-to-Case | per diode | -- | -- | 0.9 | °C/W |
| | | per module | -- | -- | 0.15 | °C/W |
| R _{θCS} | Thermal Resistance Case -to-Sink | per diode | -- | -- | 0.48 | °C/W |
| | | per module | -- | -- | 0.08 | °C/W |

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■ Thyristor

ABSOLUTE MAXIMUM RATINGST_C=25°C unless otherwise specified

| Symbol | Test Condition | Value | Unit |
|--------------------|--|----------|-------------------|
| V _{RRM} | | 1600 | V |
| I _{T(AV)} | T _C =90°C, 180° conduction, half sine wave; | 75 | A |
| I _{TSM} | T _J =45°C, t=10ms (50Hz), sine, V _R =V _{RRM} ; | 1200 | A |
| | T _J =45°C, t=8.3 ms (60Hz), sine, V _R = V _{RRM} ; | 1300 | |
| I ² t | T _J =45°C, t=10ms (50Hz), sine, V _R =V _{RRM} ; | 7.2 | kA ² s |
| | T _J =45°C, t=8.3 ms (60Hz), sine, V _R = V _{RRM} ; | 7.0 | |
| dV/dt | T _J =125°C, exponential to 67% rated V _{DRM} | 1000 | V/us |
| dI/dt | T _J =125°C, I _{TM} =200A, rated V _{DRM} | 150 | A/us |
| V _{ISOL} | 50Hz, all terminals shorted, t=1s, I _{ISOL} ≤1mA ; | 3000 | V~ |
| T _J | Max. junction operating temperature range | -40~125 | °C |
| T _{STG} | Max. storage temperature range | -40~125 | °C |
| | Mounting torque(M6) | 3 to 5 | N·m |
| | Terminal connection torque(M5) | 2.5 to 5 | N·m |

ELECTRICAL AND THERMAL CHARACTERISTICST_C=25°C unless otherwise specified

| Symbol | Test Condition | Min. | Typ. | Max. | Unit |
|------------------------------------|--|------|------|------|------|
| I _{DRM} /I _{RRM} | T _J =125°C, V _D =V _R =1600V; | | | 20 | mA |
| V _{TM} | I _{TM} =235A, t _d =10 ms, half sine; | | 1.50 | | V |
| V _{GT} | V _A =6V, R _A =1Ω, T _J =-40°C; | | | 4 | V |
| | V _A =6V, R _A =1Ω; | | | 2.5 | |
| | V _A =6V, R _A =1Ω, T _J =125°C; | | | 1.7 | |
| I _{GT} | V _A =6V, R _A =1Ω, T _J =-40°C; | | | 270 | mA |
| | V _A =6V, R _A =1Ω; | | | 150 | |
| | V _A =6V, R _A =1Ω, T _J =125°C; | | | 80 | |
| P _{GM} | t _p ≤5ms, T _J =125°C; | | | 12 | W |
| P _{GM(AV)} | f=50Hz, T _J =125°C; | | | 3 | W |
| R _{thjc} | Thermal Resistance , Junction-to-Case | | | 0.35 | K/W |
| R _{THCS} | Thermal Resistance, Case -to-Sink | | | 0.15 | K/W |

Characteristic curves

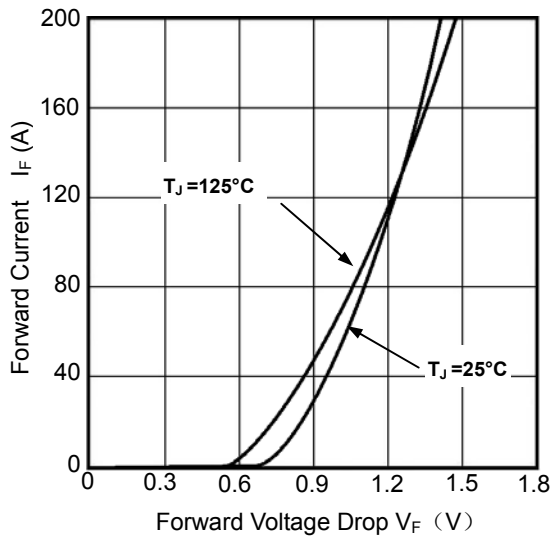


Figure 1. Diode Forward Voltage Drop vs Forward Current

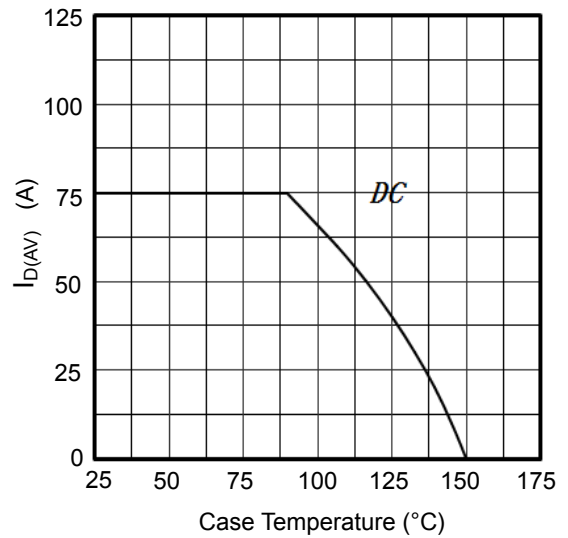


Figure 2. Diode $I_{D(AV)}$ vs Case Temperature

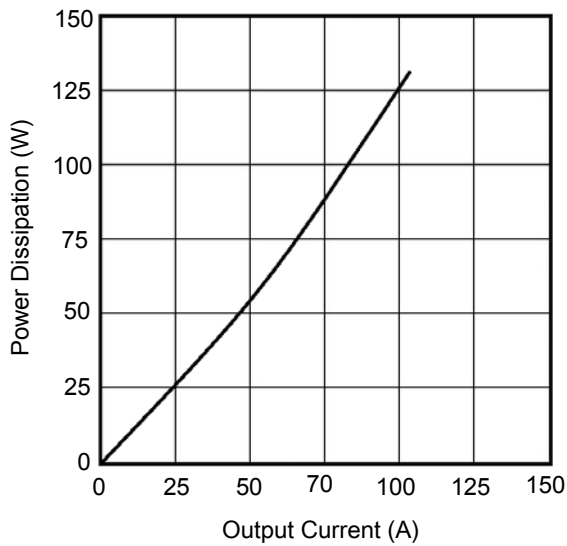


Figure 3. SCR Output Current vs Power Dissipation

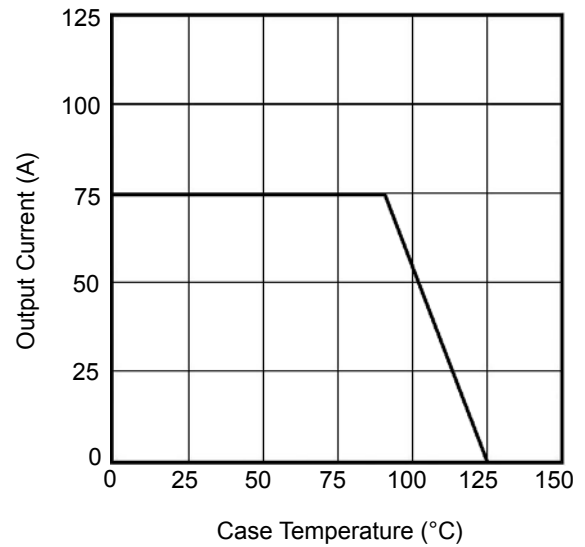


Figure 4. SCR Output Current vs Case Temperature

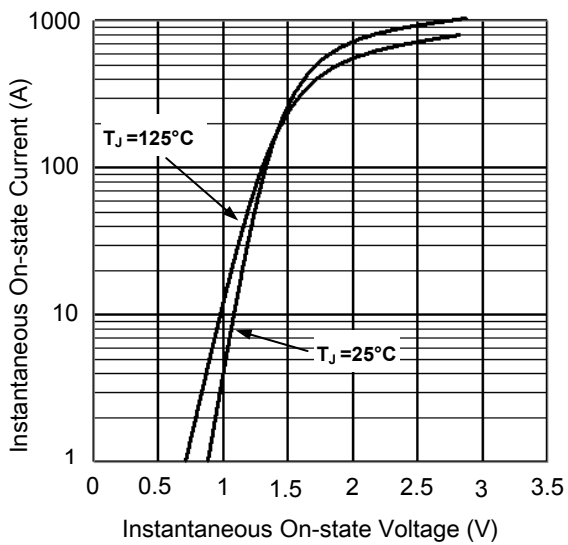


Figure 5. SCR On State Voltage Drop

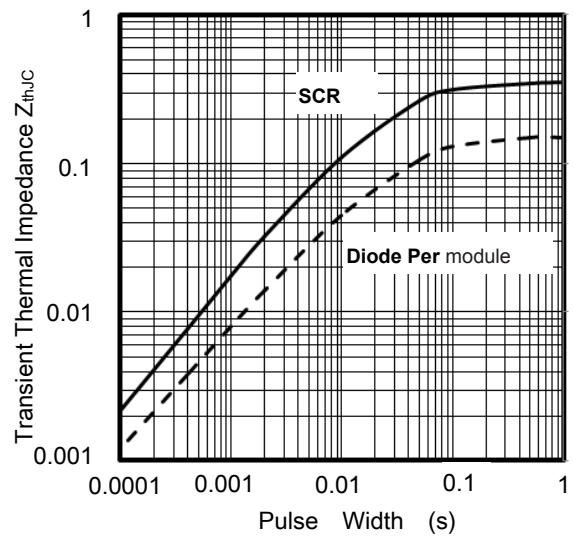


Figure 6. Diode and SCR Thermal Impedance Z_{thJC}

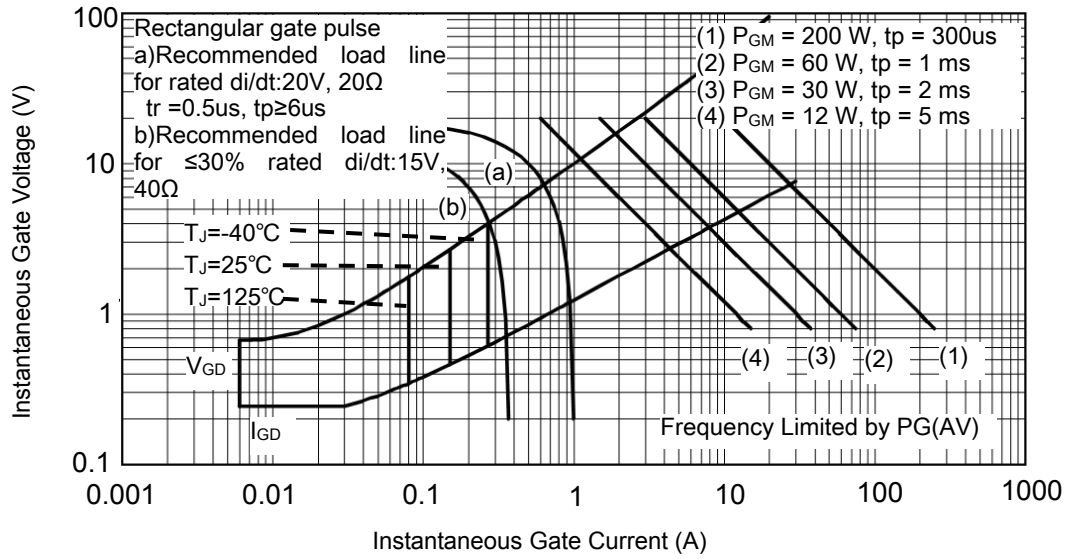


Figure 7. Gate Characteristics

Package Outline (Dimensions in mm)

